APPLICA

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

TC 2800 MAIL ROOM

the Application of

Seiichi MIYAZAKI

Application No.:

09/913,334

Docket No.:

110386

Filed: August 13, 2001

For:

ETCHANT, ETCHING METHOD AND SEMICONDUCTOR SILICON WAFER

REQUEST FOR CORRECTION OF PALM RECORDS

Director of the U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

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WPB:TJP/cmm

Date: October 30, 2001

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TOT CLAIMS

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Oliff & Berridge PO Box 19928 Alexandria, VA 22320

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Seiichi Miyazaki, Kosyoku-shi, JAPAN;

Domestic Priority data as claimed by applicant

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